

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: Docket No.: TI-35428  
Gerard Chauvel Art Unit: 2183  
Serial No: 10/632,084 Examiner: Coleman, Eric  
Filed: July 31, 2003 Conf. No.: 2214  
For: Test and skip processor instruction having at least one register operand

**AMENDMENT PURSUANT TO 37 CFR 1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant has received the Office Action dated 5/12/2010, which rejected Claims 1-25. Please amend the above-identified patent application as follows:

Amendments to the Clams begin on page 2; and  
Remarks begin on page 6.